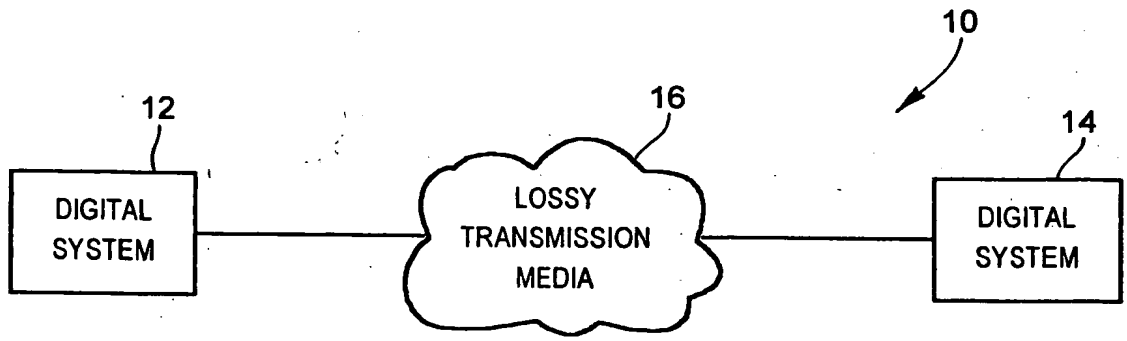

FIG. 1

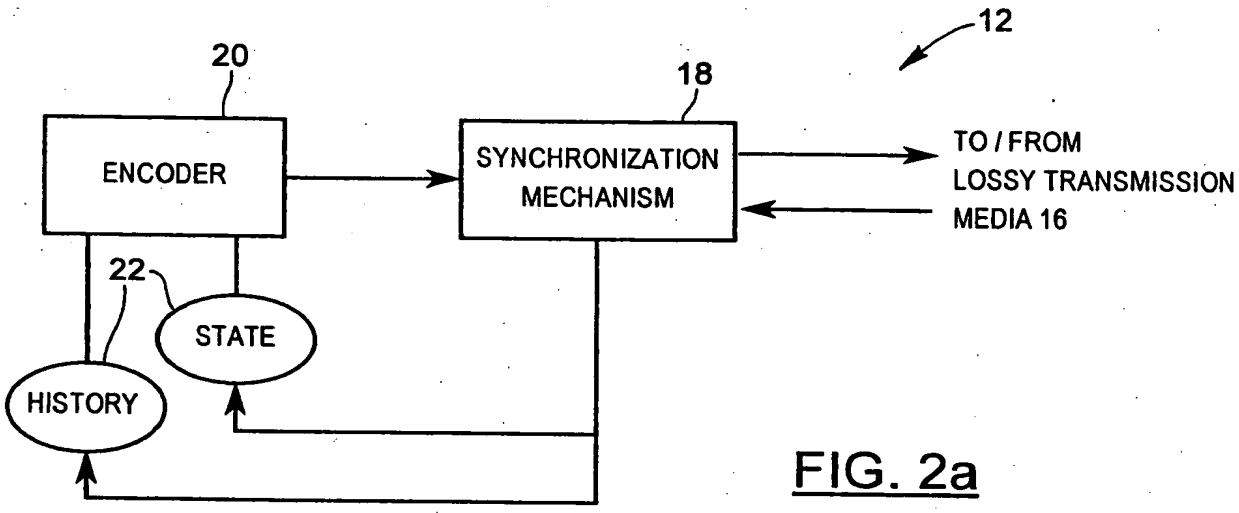

FIG. 2a

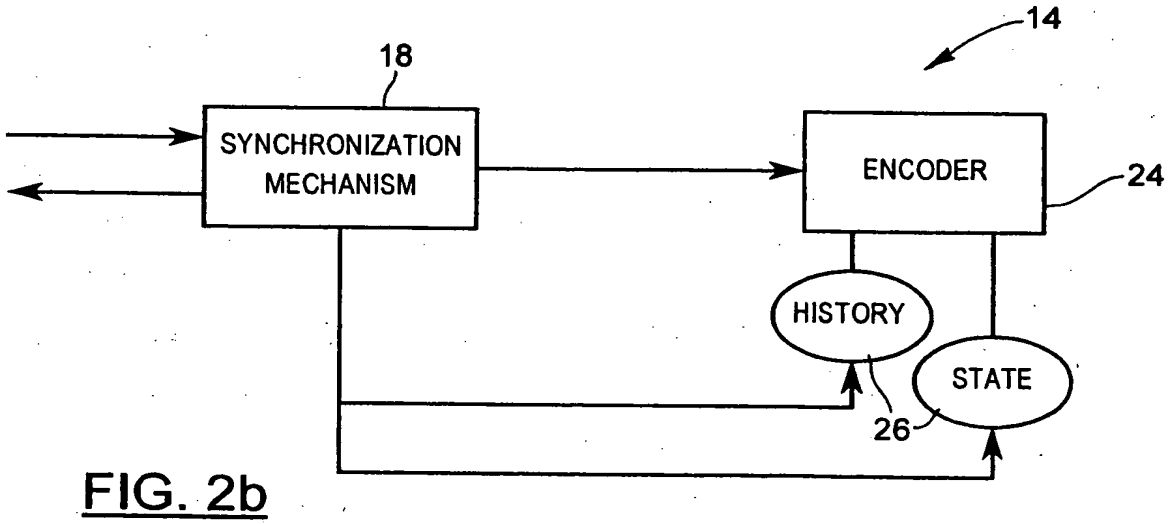

FIG. 2b

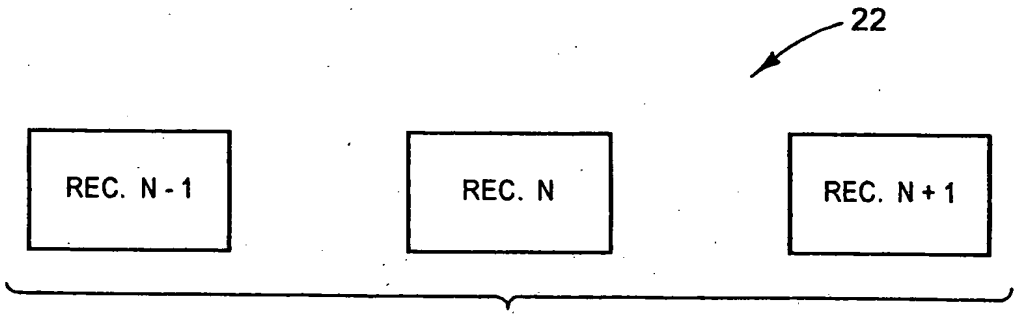
22
REC. N - 1
REC. N
REC. N + 1

FIG. 3a

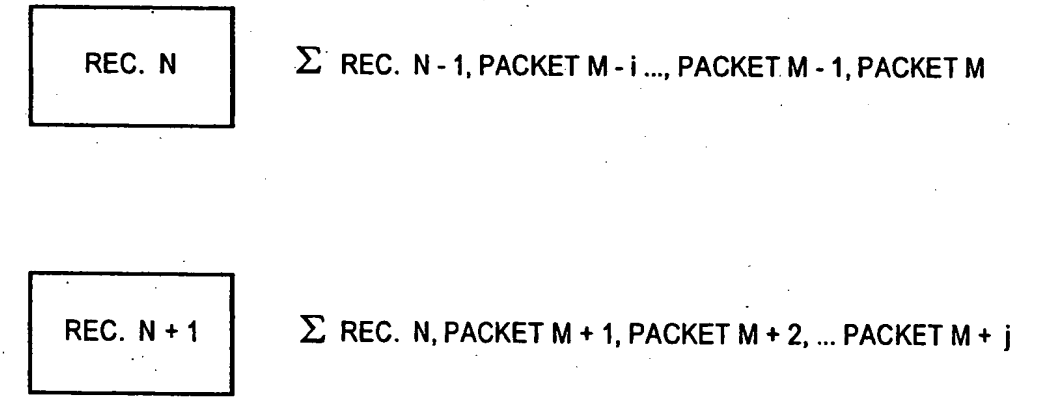
REC. N
Σ REC. N - 1, PACKET M - i ..., PACKET M - 1, PACKET M
REC. N + 1
Σ REC. N, PACKET M + 1, PACKET M + 2, ... PACKET M + j

FIG. 3b

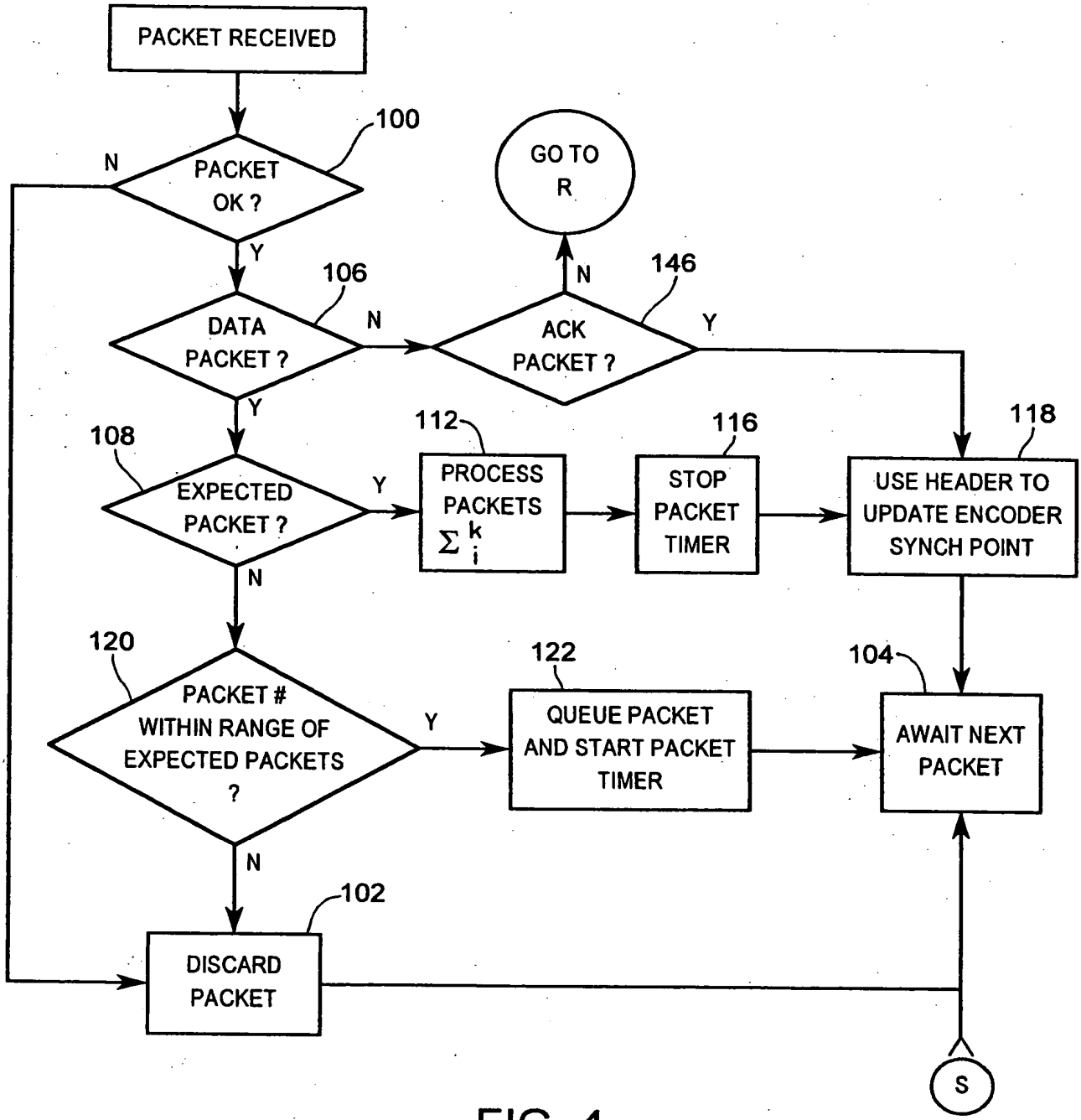
PACKET RECEIVED
100
PACKET OK ?
N
Y
GO TO R
106
DATA PACKET ?
N
Y
146
ACK PACKET ?
N
Y
108
EXPECTED PACKET ?
Y
N
112
PROCESS PACKETS Σ_i^k
116
STOP PACKET TIMER
118
USE HEADER TO UPDATE ENCODER SYNCH POINT
120
PACKET # WITHIN RANGE OF EXPECTED PACKETS ?
Y
N
122
QUEUE PACKET AND START PACKET TIMER
104
AWAIT NEXT PACKET
102
DISCARD PACKET
S

FIG. 4

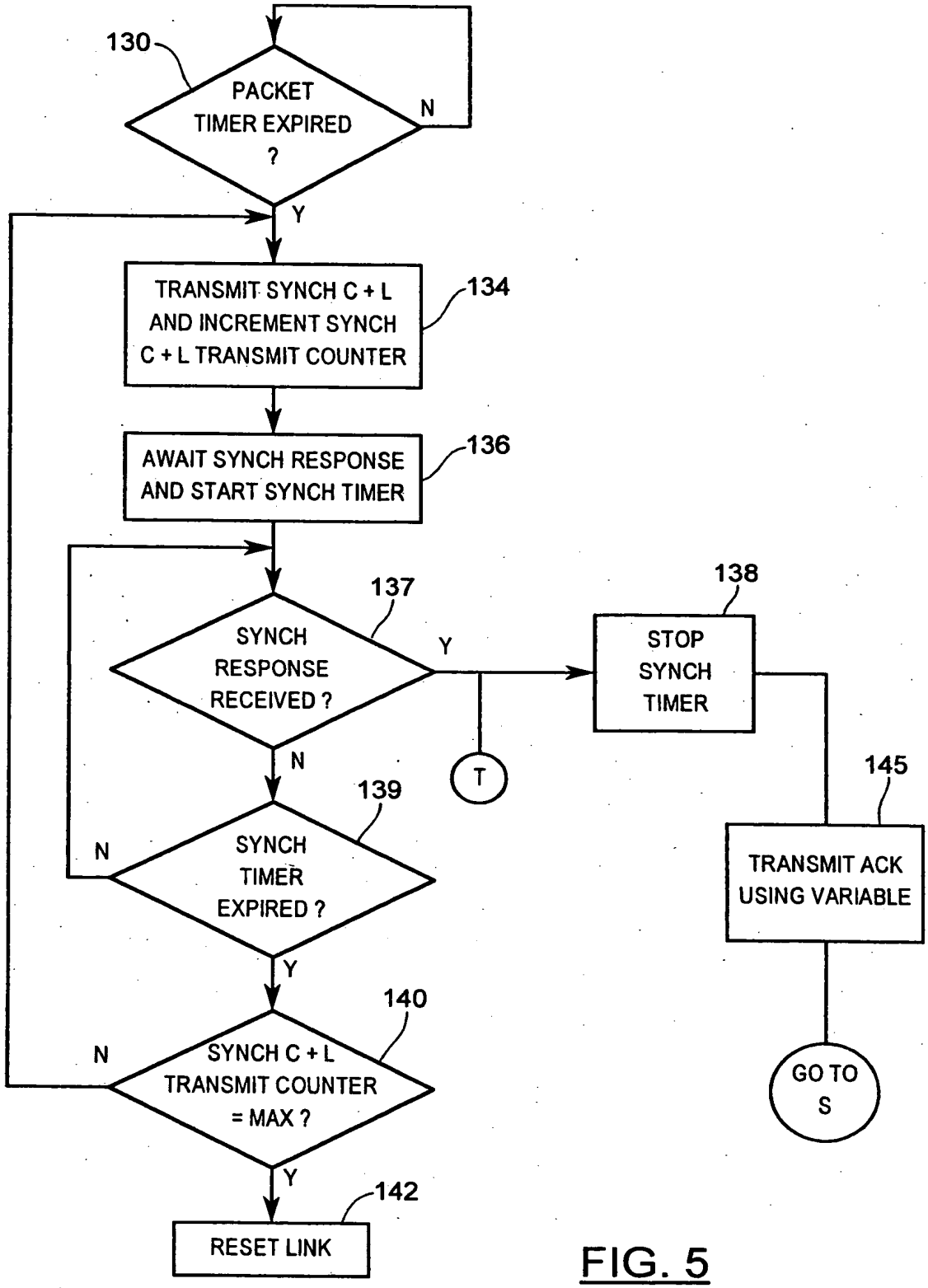
130
PACKET TIMER EXPIRED ?
N
Y
TRANSMIT SYNCH C + L AND INCREMENT SYNCH C + L TRANSMIT COUNTER
134
AWAIT SYNCH RESPONSE AND START SYNCH TIMER
136
137
SYNCH RESPONSE RECEIVED ?
Y
138
STOP SYNCH TIMER
N
T
145
139
SYNCH TIMER EXPIRED ?
N
TRANSMIT ACK USING VARIABLE
Y
140
SYNCH C + L TRANSMIT COUNTER = MAX ?
N
GO TO S
Y
142
RESET LINK

FIG. 5

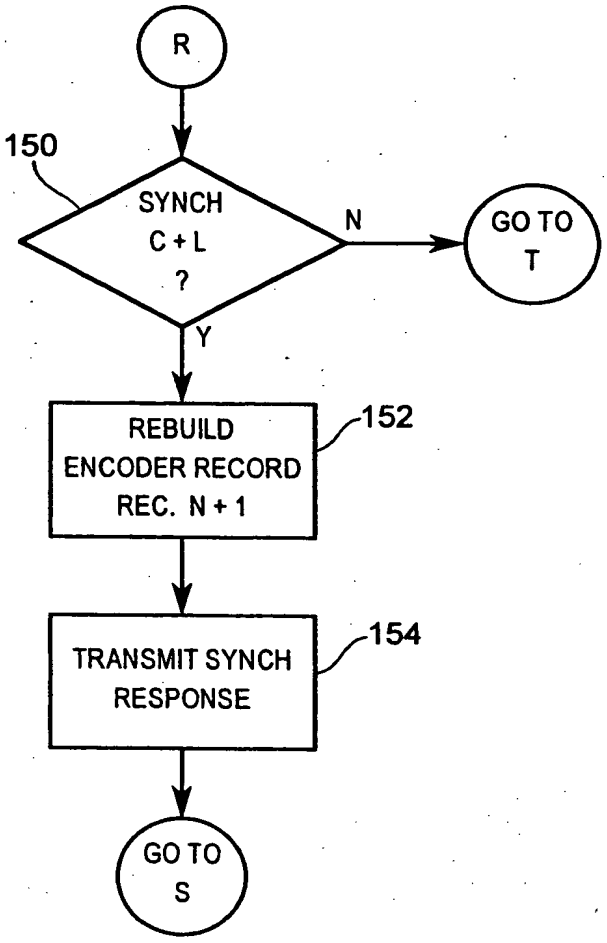
R
150
SYNCH
C + L
?
N
GO TO
T
Y
REBUILD
ENCODER RECORD
REC. N + 1
152
TRANSMIT SYNCH
RESPONSE
154
GO TO
S

FIG. 6